

*IN THE UNITED STATES PATENT AND TRADEMARK OFFICE*

Applicant: Yasushi AMAMIYA

Title: LOGIC CIRCUIT WITH RESTRAINED LEAK CURRENT TO  
DIFFERENTIAL CIRCUIT

Appl. No.: Unassigned

Filing Date: 12/15/2005

Examiner: Unassigned

Art Unit: Unassigned

PRELIMINARY AMENDMENT UNDER 37 CFR 1.115

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Prior to examination of the present Application, Applicant respectfully requests that the application be amended as follows:

**Amendments to the Specification** begin on page 2 of this document.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this document.

**Remarks/Arguments** begin on page 10 of this document.

Please amend the application as follows: